Interference

	Туре	L#	Hits	Search Text
1	BRS	L1	2	(grid same monitors) and (design same simulation) and (design same verific\$6) and (ordered adj axis)
2	BRS	L2	2	(grid same monitors) and (design same simulation) and (design same verific\$6) and (ordered adj axis)
3	BRS	L3	3	(grid same declar\$6) and (integer same axis)

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